

NOTICE OF ALLOWANCE

This is a Notice of Allowance in response to the Amendment filed 4/11/2008.

Claims 1-25 were rejected in the prior Office Action.

Claims 5, 7-12, 15, 17-20 and 24 have been cancelled.

Allowable Subject Matter

Claims 1-4, 6, 13, 14, 16, 21-23 and 25 are allowed. Renumbered Claims 1-12.

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

The prior arts of record taken alone or in combination fail to teach, anticipate, suggest or render obvious a method and a circuit recited in the independent claim 1, 13 and 21 as currently amended, for implementing at speed bit fail mapping of an embedded memory system having a BIST (Built In Self Testing) engine, including among other limitations, "implementing a BIST clock control logic, in response to the generated signal, for automatically pausing the BIST bit fail testing upon recognition of a fail of the embedded memory system, the BIST clock control logic gating off the high speed multiplied clock to BIST test latches and fail location latches of the diagnostic register".

Consequently, Claims 1-4, 6, 13, 14, 16, 21-23 and 25 are allowed over the prior arts of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAMES C. KERVEROS whose telephone number is (571) 272-3824. The examiner can normally be reached on 9:00 AM TO 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques H. Louis-Jacques can be reached on (571) 272-4150. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/JAMES C KERVEROS/
Primary Examiner, Art Unit 2117

Date: 20 June 2008
Office Action: Allowance

U.S. Patent & Trademark Office
Alexandria, VA 22314.
Tel: (571) 272-3824, Fax: (571) 273-3824
Email: james.kerveros@uspto.gov